========== Test 1 ==========

Verify that the differents Modes of the RPS give correct Outputs on the RPS module, @ Tue Feb 27 05:05:56 CET 2018 by nachtschicht

1. set RPS mode for beamline bis BMA1: mode to "2,IQCOM,$BMA1,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x000C
   Interlocks outputs (5–8) of J102003 have the value 0x0000

2. set RPS mode for beamline bis BME1: mode to "2,IQCOM,$BME1,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x000C
   Interlocks outputs (5–8) of J102003 have the value 0x0000

3. set RPS mode for beamline bis BMC1: mode to "2,IQCOM,$BMC1,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x000C
   Interlocks outputs (5–8) of J102003 have the value 0x0000

4. set RPS mode for beamline Gantry 1: mode to "2,IQCOM,$GNT1,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x00CC
   Interlocks outputs (5–8) of J102003 have the value 0xC000

5. set RPS mode for beamline Gantry 2: mode to "2,IQCOM,$GNT2,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x000C
   Interlocks outputs (5–8) of J102003 have the value 0x00CC

6. set RPS mode for beamline Optis: mode to "2,IQCOM,$OPT,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0xC00C
   Interlocks outputs (5–8) of J102003 have the value 0x0C00

7. set RPS mode for beamline bis BMD1: mode to "2,IQCOM,$BMD1,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x000C
   Interlocks outputs (5–8) of J102003 have the value 0x0000

8. set RPS mode for beamline Experiment: mode to "2,IQCOM,PIF,1,DIA"
   Interlocks outputs (1–4) of J102003 have the value 0x0C0C
   Interlocks outputs (5–8) of J102003 have the value 0x0CC0

9. set RPS mode for beamline Gantry 3: mode to "2,IQCOM,$GNT3,1,DIA"

Interlocks outputs (1–4) of J102003 have the value 0x0C0C
Interlocks outputs (5–8) of J102003 have the value 0x0CC0

========== Test was successfull ==========

========== Test 2 ==========
Verify that for different modes, the RPS behaves correctly
when scanning AMA1 @ Tue Feb 27 05:05:56 CET 2018 by nachtschicht

set RPS mode for beamline Gantry 2: mode to "2,IQCOM,$GNT2,1,DIA"

for AMA1<1000 inputs and outputs of module J102025 as expected: inputs

set RPS mode for beamline Gantry 1: mode to "2,IQCOM,$GNT1,1,DIA"
Start scan with AMA1

for AMA1<1000 inputs and outputs of module J102025 as expected 0x1DDD

Test 2 has not been successfull, Repeat, Fix or inform ZPT

========== Test 3 ==========
procedure AMA3 not implemented @ Tue Feb 27 05:05:56 CET 2018

========== Test 4 ==========
procedure AMA3 not implemented @ Tue Feb 27 05:05:56 CET 2018

========== Test 5 ==========
Test Interlock generation of diagnostic modules
  @ Tue Feb 27 05:05:56 CET 2018 by nachtschicht

  master on RPS module J102025
  read RPS module input J102025:REG:R:7
  generate pulse on module XMA1118
  read RPS module input J102025:REG:R:7
  Pulse gave the expected value of 0x10DD

  master on RPS module J102007

read RPS module input J102007:REG:R:4
generate pulse on module XMA1119
read RPS module input J102007:REG:R:4
Pulse gave the expected value of 0xDDDD

master on RPS module J102007
read RPS module input J102007:REG:R:4
generate pulse on module XMA1116
read RPS module input J102007:REG:R:4
Pulse gave the expected value of 0xDDDD

master on RPS module J102007
read RPS module input J102007:REG:R:4
generate pulse on module XMA1115
read RPS module input J102007:REG:R:4
Pulse gave the expected value of 0xDDDD

master on RPS module J102007
read RPS module input J102007:REG:R:4
generate pulse on module XMA1117
read RPS module input J102007:REG:R:4
Pulse gave the expected value of 0xDDDD

master on RPS module J112009
read RPS module input J112009:REG:R:6
generate pulse on module XME1310
read RPS module input J112009:REG:R:6
Pulse gave the expected value of 0xD0DD

master on RPS module J112023
read RPS module input J112023:REG:R:4
generate pulse on module XME1311
read RPS module input J112023:REG:R:4
Pulse gave the expected value of 0xDDDD

master on RPS module J112023
read RPS module input J112023:REG:R:5

generate pulse on module XME1410
read RPS module input J112023:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J112023
read RPS module input J112023:REG:R:5
generate pulse on module XME1411
read RPS module input J112023:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J101030
read RPS module input J101030:REG:R:5
generate pulse on module XMA0310
read RPS module input J101030:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J101030
read RPS module input J101030:REG:R:5
generate pulse on module XMA0311
read RPS module input J101030:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J101046
read RPS module input J101046:REG:R:7
generate pulse on module XMA0410
read RPS module input J101046:REG:R:7
Pulse gave the expected value of 0x10D0

master on RPS module J124081
read RPS module input J124081:REG:R:5
generate pulse on module XMD0710
read RPS module input J124081:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J124081
read RPS module input J124081:REG:R:5
generate pulse on module XMD0711

read RPS module input J124081:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J124092
read RPS module input J124092:REG:R:4
generate pulse on module XMD0717
read RPS module input J124092:REG:R:4
Pulse gave the expected value of 0x0DDD

master on RPS module J110060
read RPS module input J110060:REG:R:5
generate pulse on module XMC0610
read RPS module input J110060:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J117065
read RPS module input J117065:REG:R:5
generate pulse on module XMC2010
read RPS module input J117065:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J117065
read RPS module input J117065:REG:R:5
generate pulse on module XMC2011
read RPS module input J117065:REG:R:5
Pulse gave the expected value of 0xDDDD

master on RPS module J108053
read RPS module input J108053:REG:R:5
generate pulse on module XMB0510
read RPS module input J108053:REG:R:5
Pulse gave the expected value of 0xDDDD

========== Test was successfull ==========